

Listing of Claims

The following listing of claims is intended to supercede all previously filed listings of claims. Changes are shown with deletions in ~~striketrough~~ and additions underlined.

Kindly enter the following amendments to the claims:

Claim 1 (currently amended). A system for coordinating the activities of one or more computers, comprising:

a reload register adapted to transmit a reload value;

a mission timer adapted to generate timer count values and to selectively generate an interrupt signal based upon said reload value transmitted from said reload register;

a first timer capture register adapted to capture a first timer count value when a first PPS signal is received; ~~and~~

~~software adapted to generate said reload value based upon said first timer count value~~

a second timer capture register adapted to capture a second timer count value when a second PPS signal is received; and

software adapted to generate a consensus PPS value based upon said first timer count value and said second timer count value and to generate said reload value based upon said consensus PPS value.

Claim 2 (cancelled).

Claim 3 (original). The system as recited in claim 1, wherein said first PPS signal is generated by a local PPS signal generator.

Claim 4 (cancelled).

Claim 5 (original). A method for coordinating the activities of one or computers, comprising:

capturing a first timer count value with a first timer capture register when a first PPS signal is received;

capturing a second timer count value with a second timer capture register when a second PPS signal is received;

generating a consensus timer count value based upon the first captured timer count value and the second captured timer count value;

generating a reload value based upon the consensus timer count value; and

generating an interrupt signal based upon the reload value.

Claim 6 (cancelled).

Claim 7 (new). The system as recited in claim 1, wherein said second PPS signal is generated by a local PPS signal generator.

Claim 8 (new). The system as recited in claim 1, wherein the period of the interrupt signal is less than one second.

Claim 9 (new). The system as recited in claim 8, wherein the period of the interrupt signal is approximately 10 ms.

Claim 10 (new). The method as recited in claim 5, wherein the period of the interrupt signal is less than one second.

Claim 11 (new). The method as recited in claim 10, wherein the period of the interrupt signal is approximately 10 ms.